

AMENDMENTS TO THE SPECIFICATION

Please replace the abstract on page 11, lines 1-10 with the following amended version:

~~The invention relates to an~~ An addressing circuit for an array arrangement (100) of electronic units (101), which may be, for example, pixels of an X-ray detector. Every pixel (101) is connected to a spatially adjacent shift register (110), the shift registers (110) being connected in turn column-wise in series for example and also being connected to a common clock line (111, 114). A trigger signal fed via an external trigger line (113) is passed by the shift registers (110) from row to row for every clock signal on the clock lines (111, 114). In this process, triggered shift registers (110) activate the associate pixels (101) so that they can be read out via read-out lines (105) that extend column-wise.

Fig. 2